

<b>Notice of References Cited</b>	Application/Control No. 10/807,274	Applicant(s)/Patent Under Reexamination AKIYAMA, KAZUTAKA	
	Examiner Leonardo Andújar	Art Unit 2826	Page 1 of 1

**U.S. PATENT DOCUMENTS**

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,770,977	08-2004	Kishida et al.	257/761
*	B	US-5,994,762	11-1999	Suwanai et al.	257/620
	C	US-			
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	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

**FOREIGN PATENT DOCUMENTS**

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**NON-PATENT DOCUMENTS**

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., Silicon Processing for the VLSI Era, 2000, Volume I, Lattice Press, 719-727, 791-795.
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\*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)  
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.